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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Supamas Sirichotiyakul et al. GROUP ART UNIT: 2123  
APPLN. NO.: 09/580,854 EXAMINER: Kandasamy Thangavelu  
FILED: May 30, 2000  
TITLE: METHODS FOR ANALYZING INTEGRATED CIRCUITS AND  
APPARATUS THEREFOR

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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Technology Center 2100**

Sir:

Responsive to the Office Action dated March 18, 2004 and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.